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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/512,149
Filing Date February 23, 2000
Inventor Vishnu K. Agarwal
Assignee Micron Technology, Inc.
Group Art Unit 2814
Examiner M. Pizarro Crespo
Attorney's Docket No. MI22-1322
Title: Integrated Circuitry Including a Capacitor with an Amorphous and a Crystalline High
K Capacitor Dielectric Region

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449


Assistant Commissioner for Patents
Washington, D.C. 20231

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449 and a copy of which is attached. No admission is made regarding whether the submitted reference is prior art.

Citation of this patent is respectfully requested.

Respectfully submitted,

Date: 8-8-02

By: 
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